



2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

TOSHIHIRO NISHII ET AL

Serial No. 10/517,445

Filed: December 9, 2004

Title: Method of manufacturing circuit
forming boards

Office of Initial Patent
Examination's Filing Receipt
Corrections

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed herewith is a copy of the Official Filing Receipt for the above-identified application marked in red indicating corrections to be made thereto.

Accordingly, it is requested that the PTO issue a Corrected Filing Receipt reflecting these corrections.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE RECD	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/517,445	12/09/2004	3726	900	2004-1930A	9	9	3

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Date Mailed: 05/19/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 000513.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/06868 05/14/2004

Foreign Applications

JAPAN 2003-140137 05/19/2003

Projected Publication Date: 08/25/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method of Manufacturing Circuit Forming Board

Circuit formation substrate manufacturing method and circuit formation substrate material

As set forth
on the Declaration
and page 1 of the
specification.

Preliminary Class

.029

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